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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit: 2144  
Serial No.: 09/476,303 Examiner: M. Thompson  
Filed : December 30, 1999  
Assignee : Intel Corporation  
Title : PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE  
PAKSET ORDERING FOR A MULTIPLE ETHERNET PORT SWITCH

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information  
Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents/publications are not being  
provided per the waiver posted on the PTO website.

Kindly accept this Information Disclosure Statement under  
Rule 97(c)(2). The rule 17(p) certification fee of \$180 is  
enclosed.

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Applicant : Gilbert Wolrich et al.  
Serial No.: 09/476,303  
Filed : December 30, 1999  
Page 2 of 2

Attorney's Docket No.:10559-133001

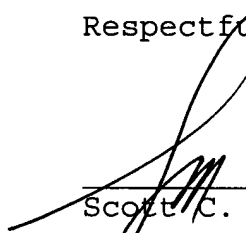
The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 11, 2005

  
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Scott C. Harris  
Reg. No. 32,030  
Attorney for Intel Corporation

Fish & Richardson P.C.  
USPTO Customer No. **20985**  
12390 El Camino Real  
San Diego, CA 92130  
Telephone: (858) 678-5070  
Facsimile: (858) 678-5099

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Substitute Form PTO-1449

U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10559-133001Application No.  
09/476,303**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

Applicant  
Gilbert Wolrich et al.Filing Date  
December 30, 1999Group Art Unit  
2144

(37 CFR §1.99(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,016,548	04/1977	Law, et al.			
	AB	4,032,899	06/1977	Jenny, et al.			
	AC	4,075,691	02/1978	Davis, et al.			
	AD	4,788,640	11/1988	Hansen			
	AE	4,831,358	05/1989	Ferrio, et al.			
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	AZ	6,463,480	10/2002	Kikuchi, et al.			
	AAA	6,463,527	10/2002	Vishkin			

Examiner Signature

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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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10559-133001

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(37 CFR \$1.98(a))

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Group Art Unit

2144

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,466,898	10/2002	Chan			
	ACC	6,529,983	03/2003	Marshall, et al.			
	ADD	6,535,878	03/2003	Guedalia, et al.			
	AEE	6,584,522	06/2003	Wolrich, et al.			
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	AKK	6,668,317	12/2003	Bernstein, et al.			
	ALL	6,671,827	12/2003	Guilford, et al.			
	AMM						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ANN							
	AOO							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	APP	"10-/100-Mbps Ethernet Media Access Controller (MAC) Core", NEC, 1998, pages 1-5.
	AQQ	"Nomadic Threads: A migrating multithread approach to remote memory accesses in multiprocessors", by Jenks, S.; Gaudiot, J.L. (abstract only) Publication Date: 20-23 Oct. 1996.
	ARR	"Overview of the START (*T) multithreaded computer" by Beckerie, M.J. (abstract only) Publication Date: 22-26 Feb. 1993.
	ASS	A. Ippoliti, et al., "Parallel Media Access Controller for Packet Communications at Gb/s Rates", 1990, IEEE, pages 991-996.
	ATT	Howard Frazier, "Gigabit Ethernet: From 100 to 1,000 Mbps", 1999, IEEE Internet Computing, pages 24-31.
	AUU	Howard Frazier, "The 802.3z Gigabit Ethernet Standard", 1998, IEEE Network, pages 6-7.

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Date Considered

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